

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Yoriharu TAKAI)
Application No.: Not Yet Assigned) Group Art Unit: Not assigned
Filed: January 28, 2004) Examiner: Not assigned
For: CONTROL CIRCUIT FOR ERROR)
CHECKING AND CORRECTION AND)
MEMORY CONTROLLER)

**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicant brings to the Examiner's attention the document listed on attached Form PTO-1449. A copy of the listed document is attached. Applicant respectfully request that the Examiner consider the document listed on attached Form PTO-1449 and indicate that it was considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

The following is a concise statement of relevance of the non-English language document:

1. Japanese Patent Application No. 2001-92723 discloses two inverter groups for an encoder and a decoder are included in an ECC control circuit, and bits thereof are inverted when necessary. Thereby, an erased region of non-volatile semiconductor memory device is not judged as an ECC error, if it is read. The relevance of this document is discussed at page 2 and 3 of the specification of the present application.

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This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitutes "prior art." If the Examiner applies the document as prior art against any claim in the application and applicant determines that the cited document does not constitute "prior art" under United States law, applicant reserves the right to present to the Office the relevant facts and law regarding the appropriate status of such document. Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: January 28, 2004

By: 

Richard V. Burgujian
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Enclosures
RVB/FPD/dvg

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INFORMATION DISCLOSURE CITATION

Atty. Docket No. 04329.3226	Application No.
Applicant Yoriharu TAKAI	
Filing Date January 28, 2004	Group: Not assigned

U.S. PATENT DOCUMENTS							
Examiner Initial*		Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS							
		Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
		2001-92723	4/6/01	Japan			No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce